

Fig. 1A

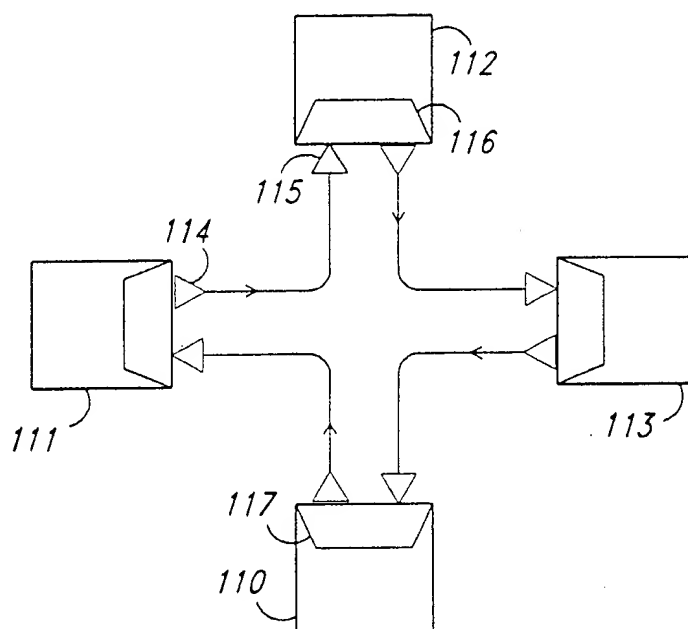


Fig. 1B

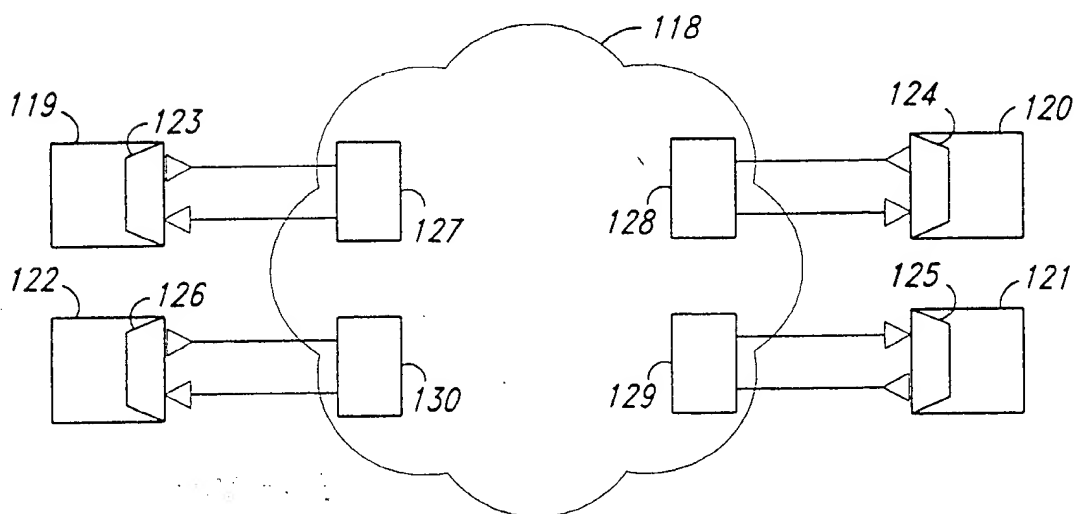
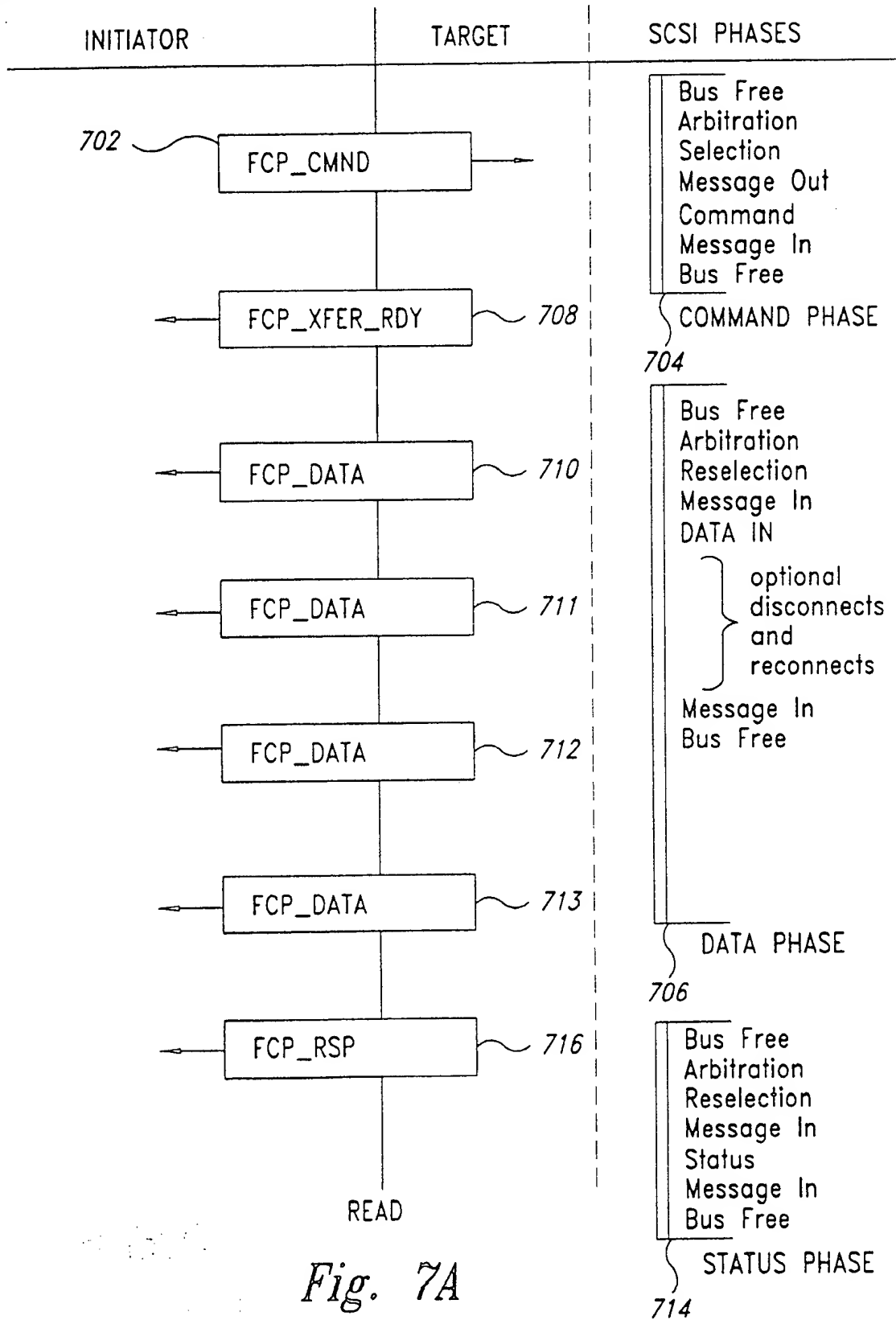


Fig. 1C



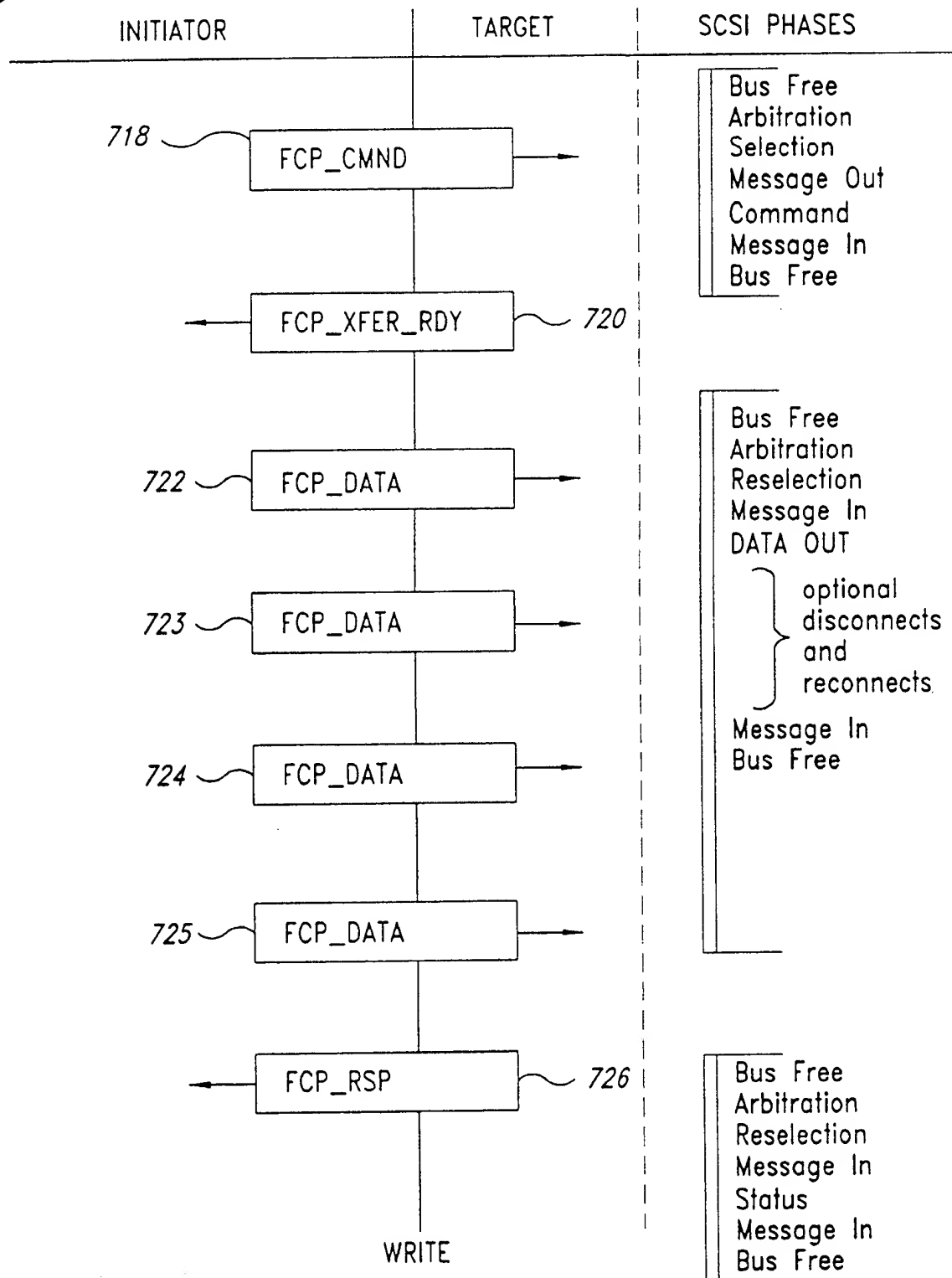


Fig. 7B

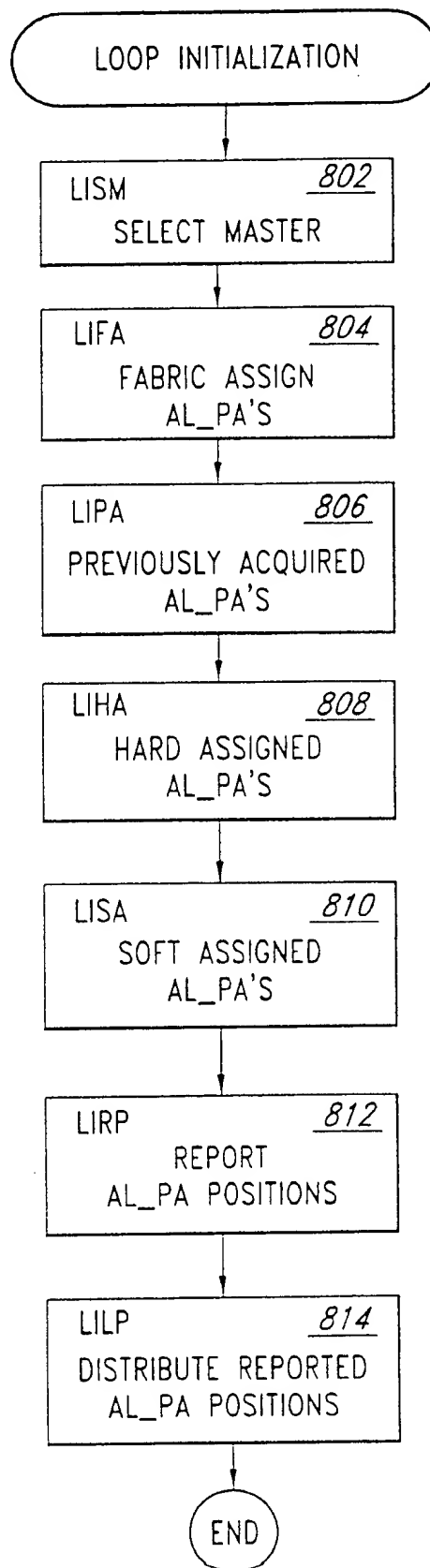


Fig. 8

JUL 07 2003
PATENT & TRADEMARK OFFICE

| 902 LI_ID | 903 LI_FL | 904 DATA |
|--------------|--------------|----------------------------------------------|
| LISM | | 8-BYTE PORT NAME 906 |
| LIFA | | 16-BYTE AL_PA BIT MAP 908 |
| LIPA | | 16-BYTE AL_PA BIT MAP 910 |
| LIHA | | 16-BYTE AL_PA BIT MAP 912 |
| LISA | | 16-BYTE AL_PA BIT MAP 914 |
| LIRP | | 128-BYTE AL_PA POSITION MAP 917 916 |
| LILP | | 128-BYTE AL_PA POSITION MAP 919 918 |

Fig. 9



Title: PRIVATE ADDRESSABLE LOOP, SELF-TEST MANAGEMENT
FOR A FIBRE CHANNEL STORAGE ENCLOSURE
Inventor(s): Timothy Anderson et al.
Application No.: 09/192,547
Docket No.: 10981774-1

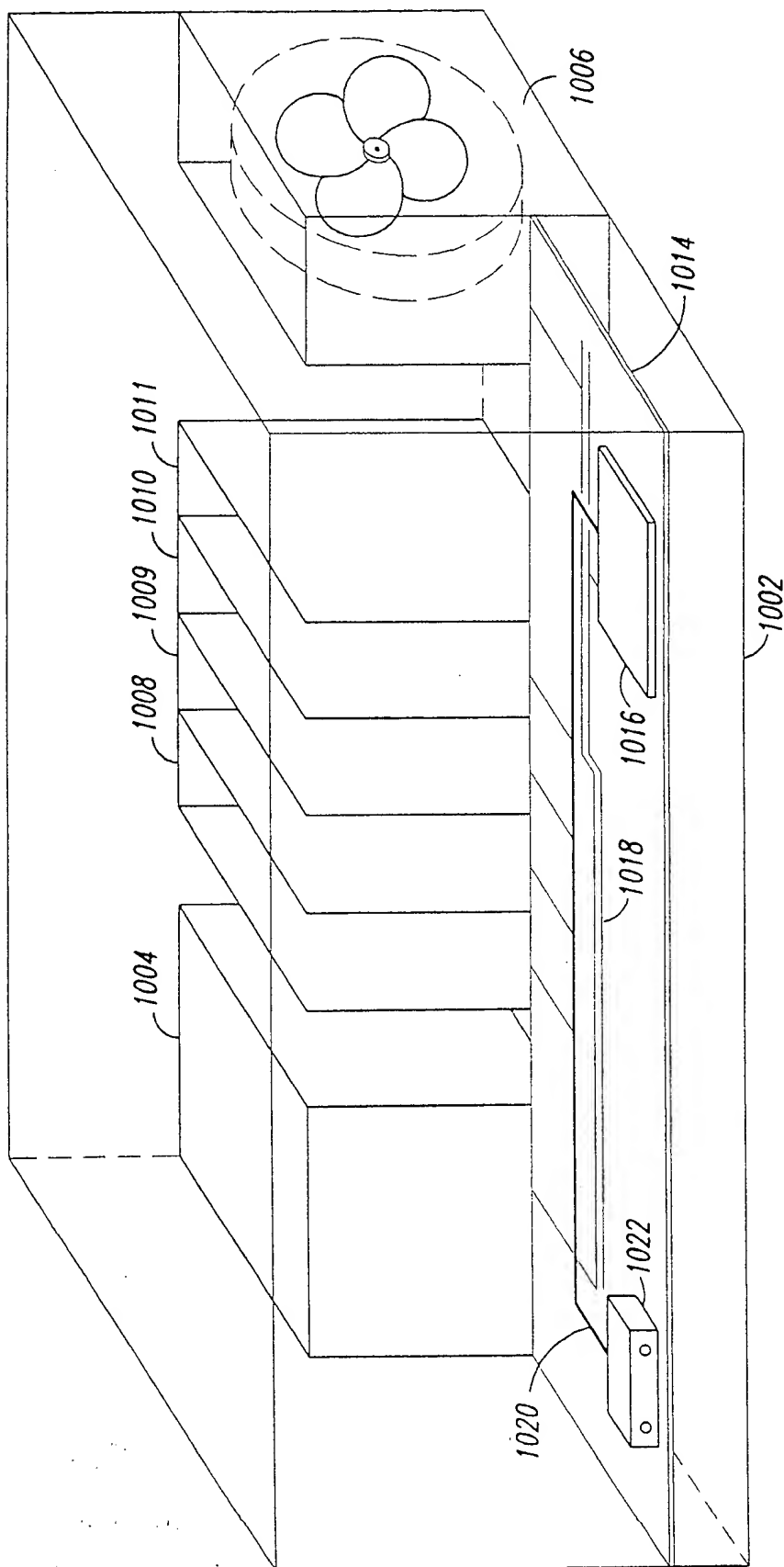


Fig. 10



Title: PRIVATE BITRATED LOOP SELF-TEST MANAGEMENT
FOR A FIBRE CHANNEL STORAGE ENCLOSURE
Inventor(s): Timothy Anderson et al.
Application No.: 09/192,547
Docket No.: 10981774-1

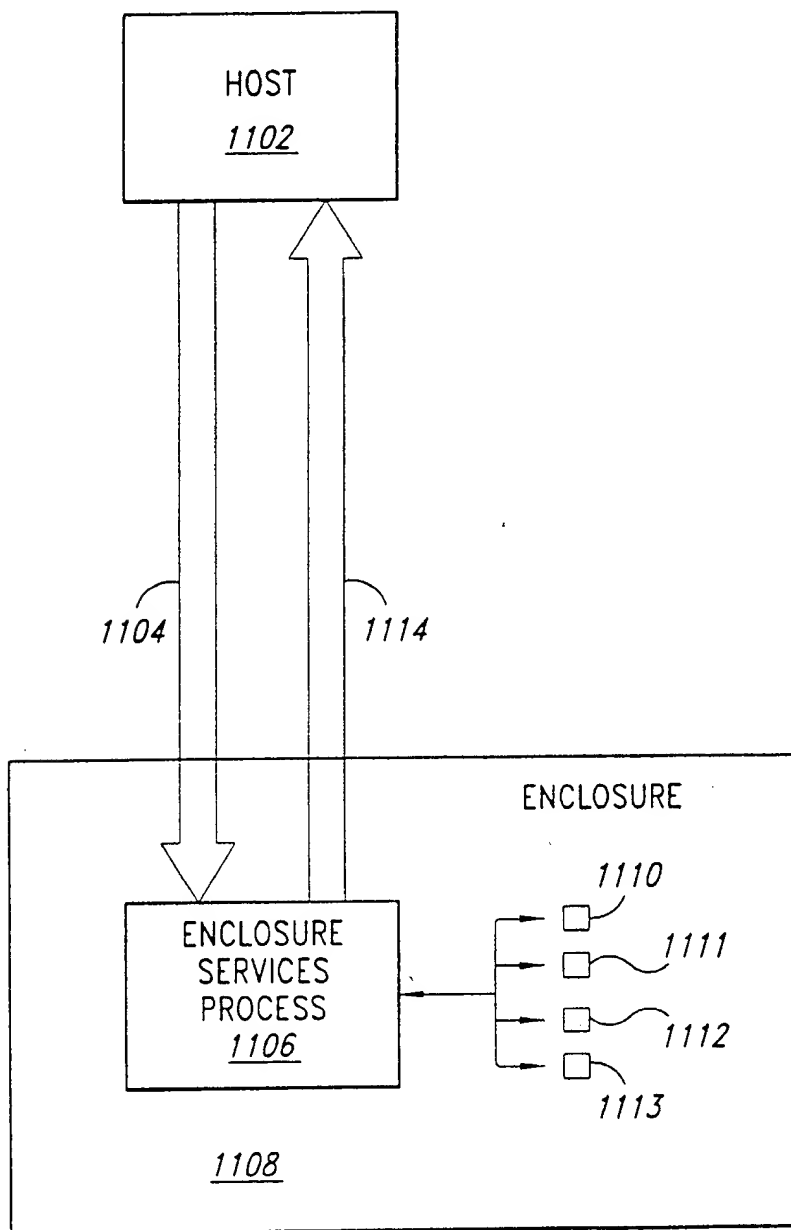
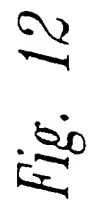
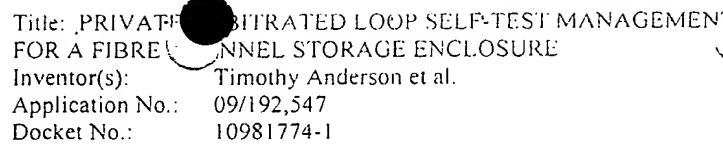


Fig. 11





Title: PRIVATE ARBITRATED LOOP SELF-TEST MANAGEMENT
FOR A FIBER CHANNEL STORAGE ENCLOSURE
Inventor(s): Timothy Anderson et al.
Application No.: 09/192,547
Docket No.: 10981774-1

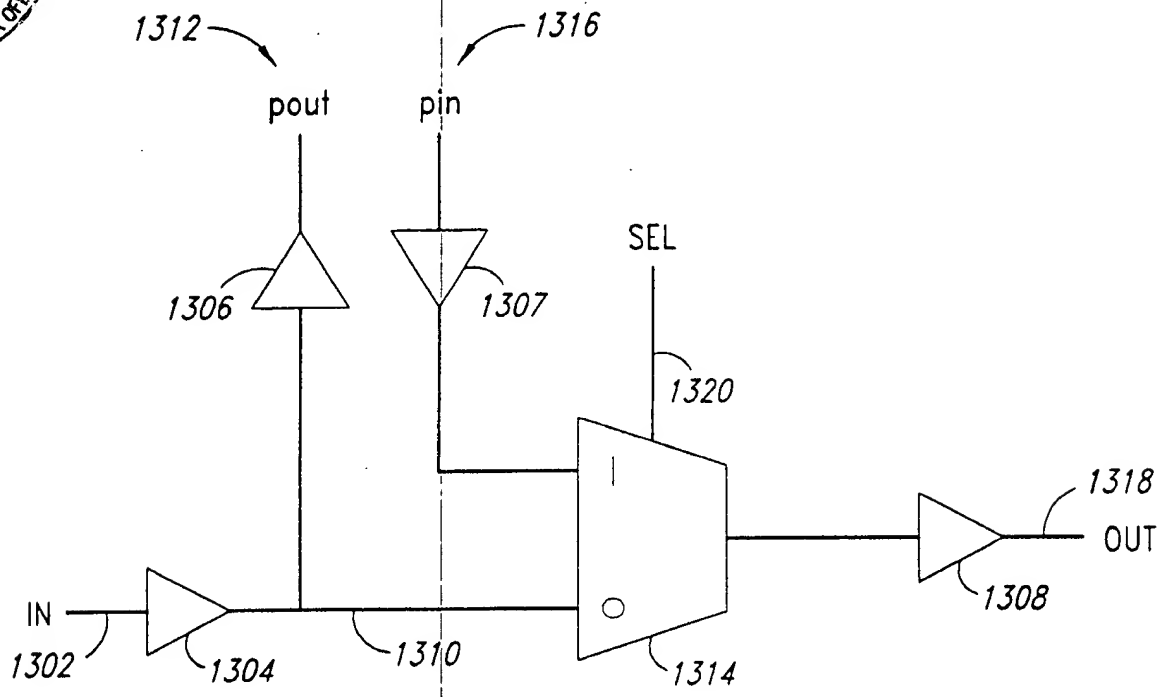


Fig. 13A

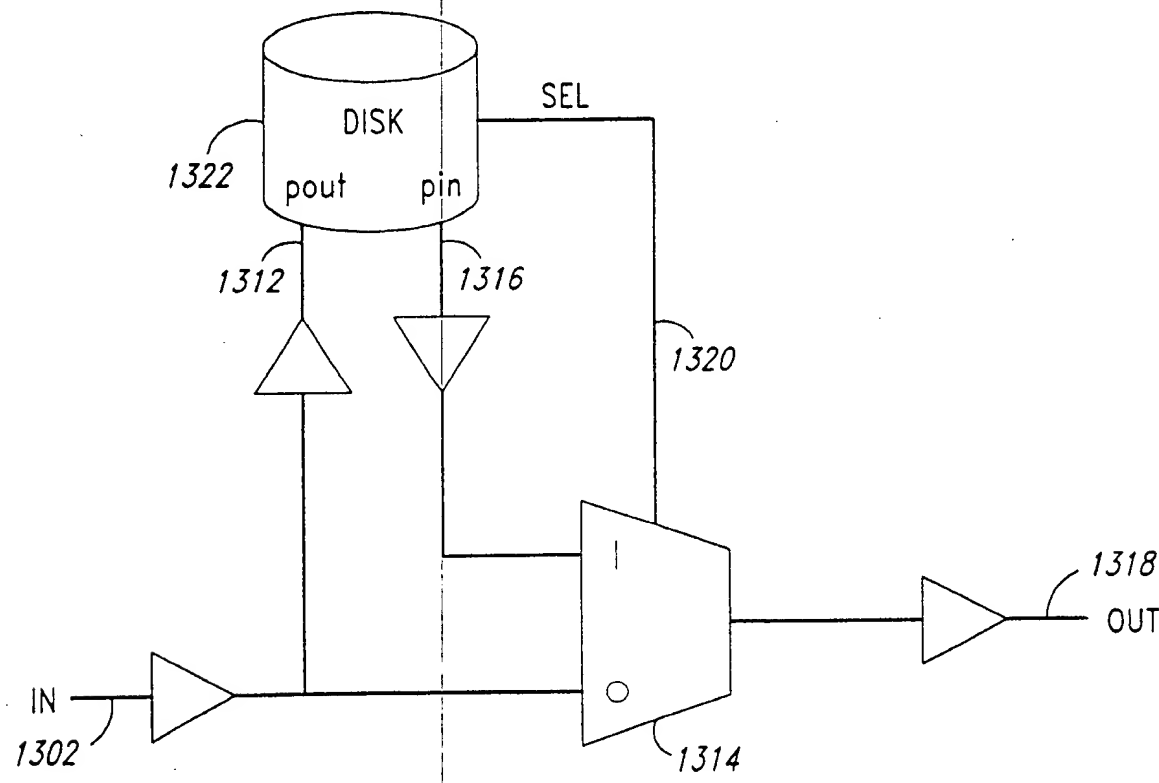
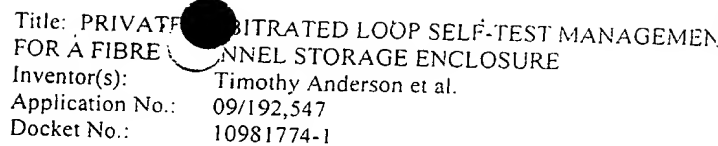


Fig. 13B



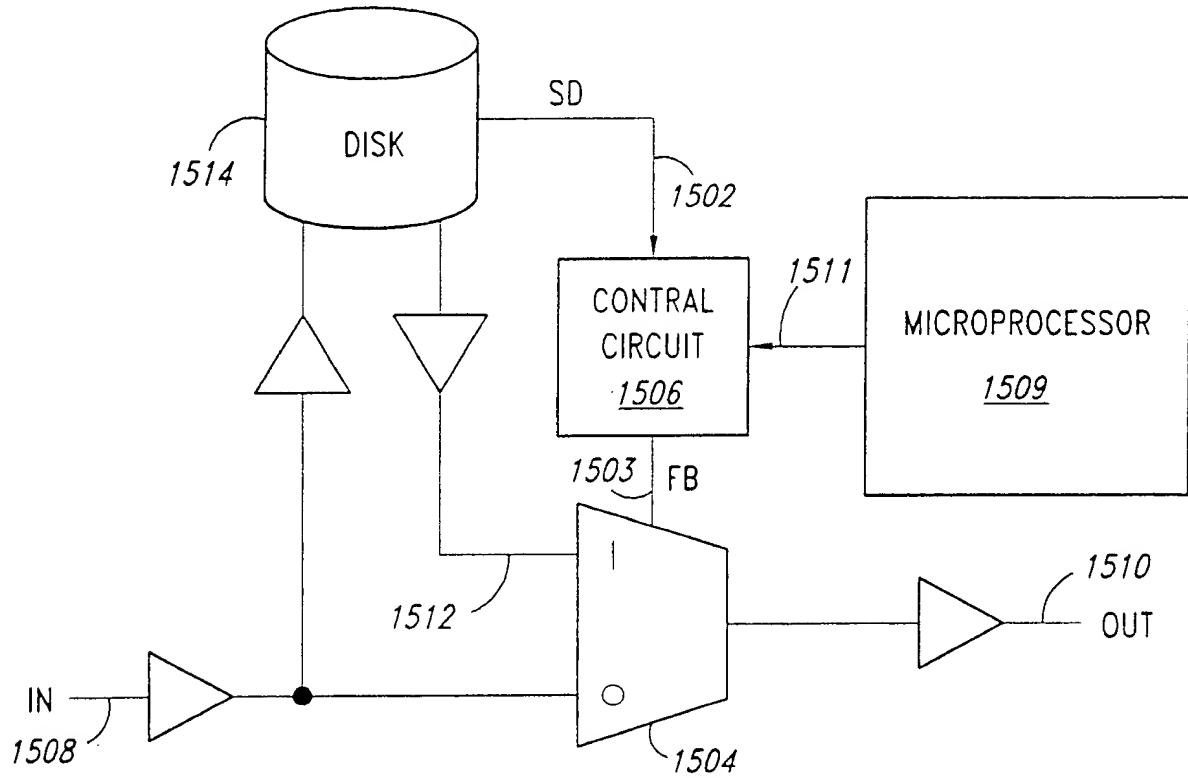


Fig. 15A

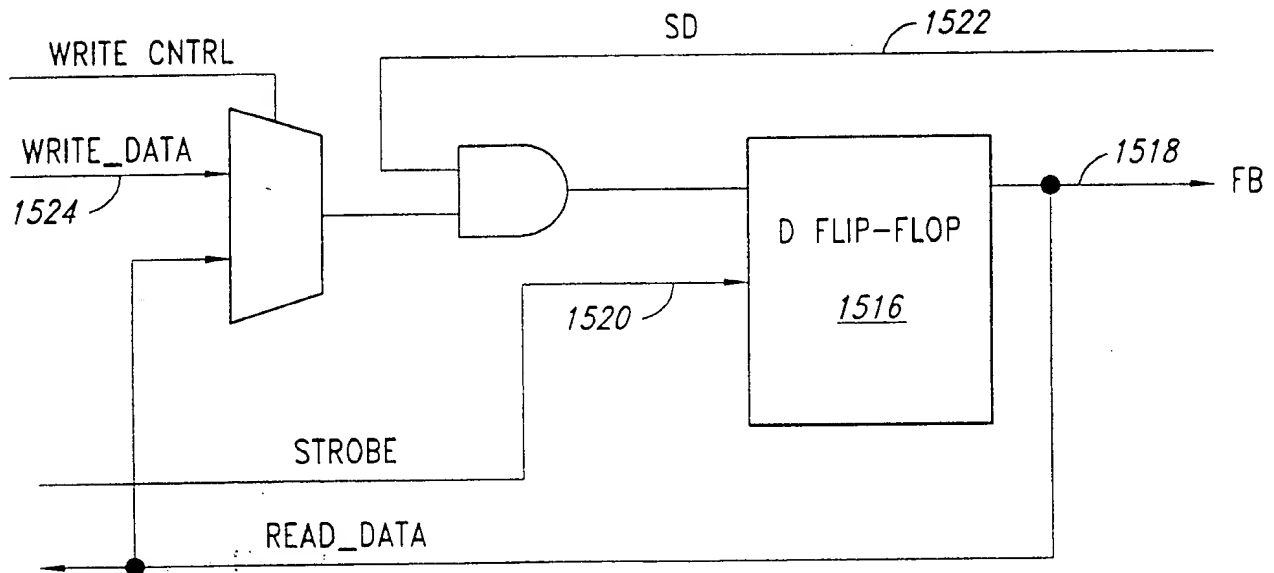


Fig. 15B



Title: PRIVACY-CONTROLLED SELF-TEST MANAGEMENT FOR A FILE CHANNEL STORAGE ENCLOSURE
Inventor(s): Timothy Anderson et al.
Application No.: 09/192,547
Docket No.: 10981774-1

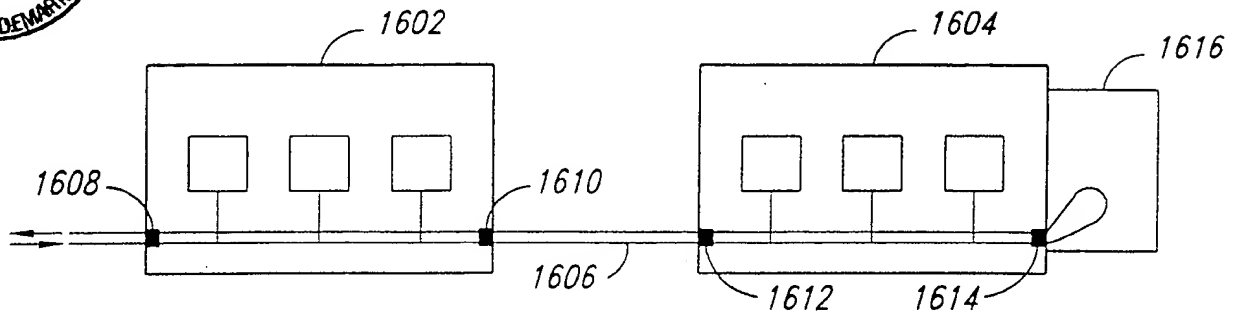


Fig. 16A

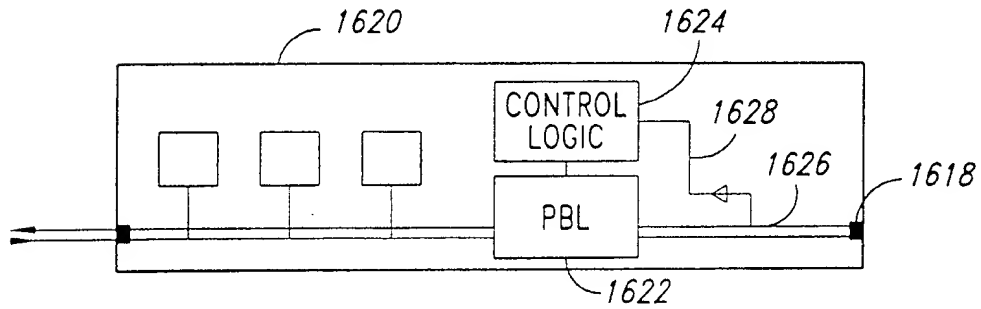


Fig. 16B

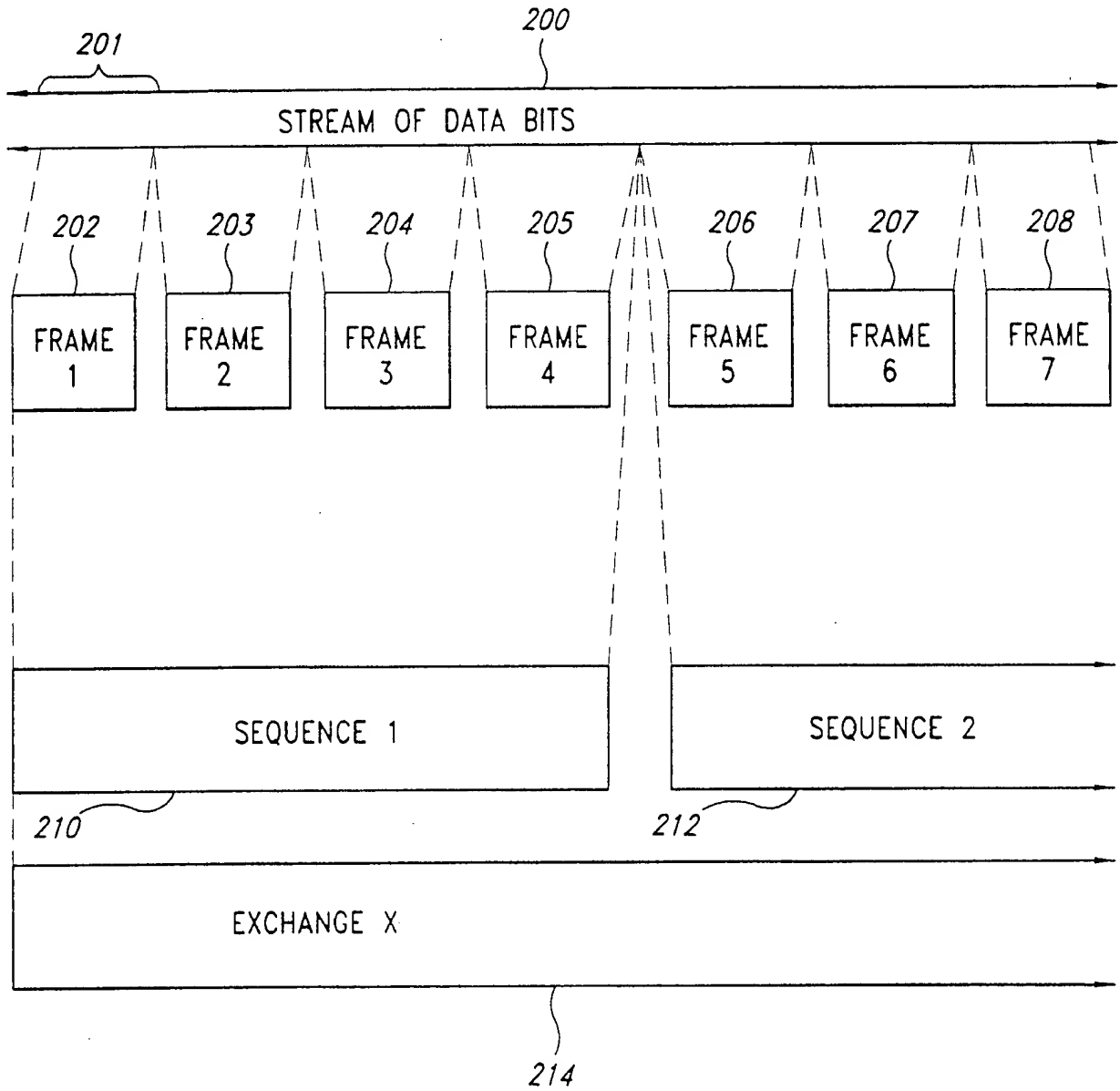


Fig. 2



Fibre Channel FCP Frame

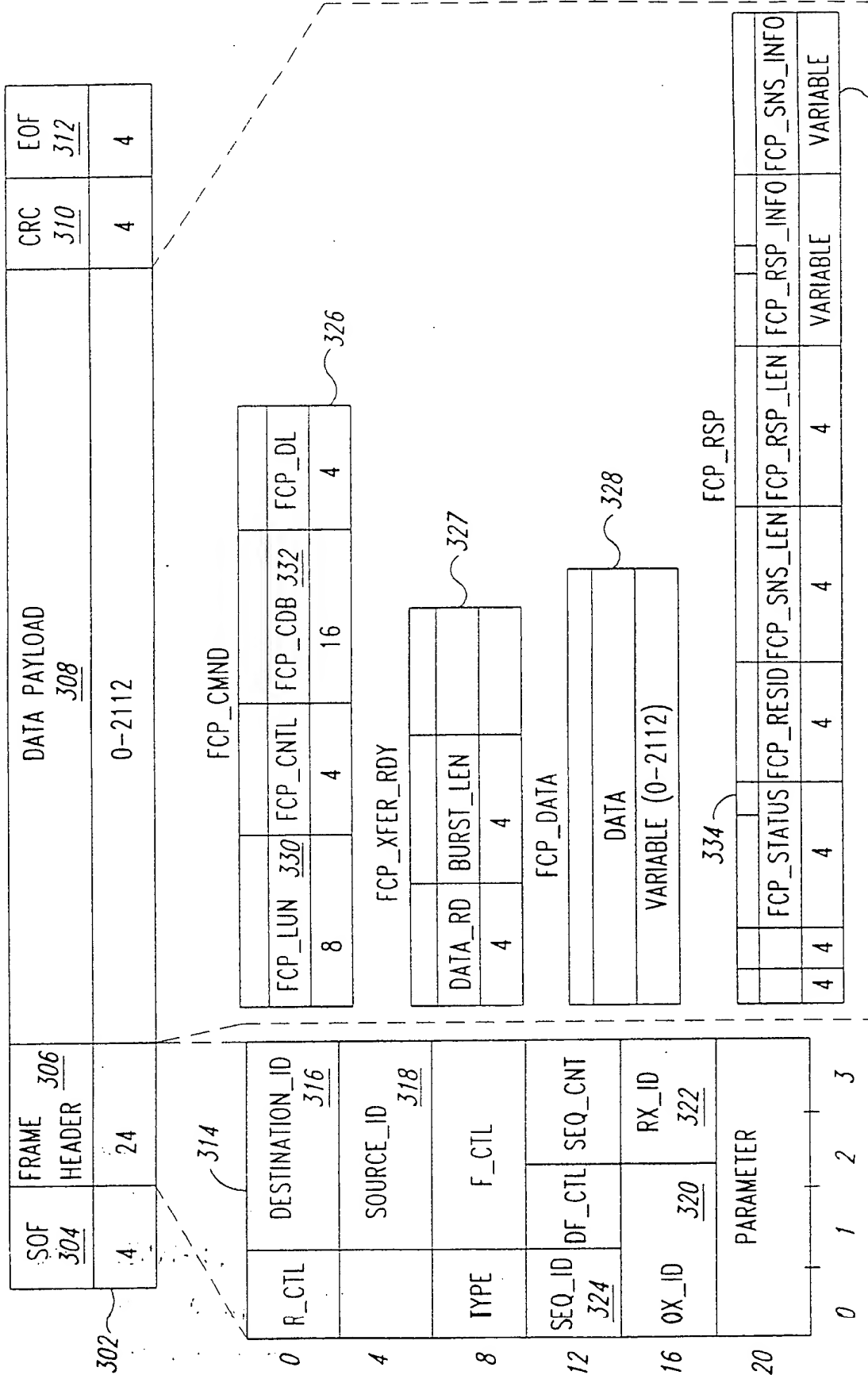


Fig. 3

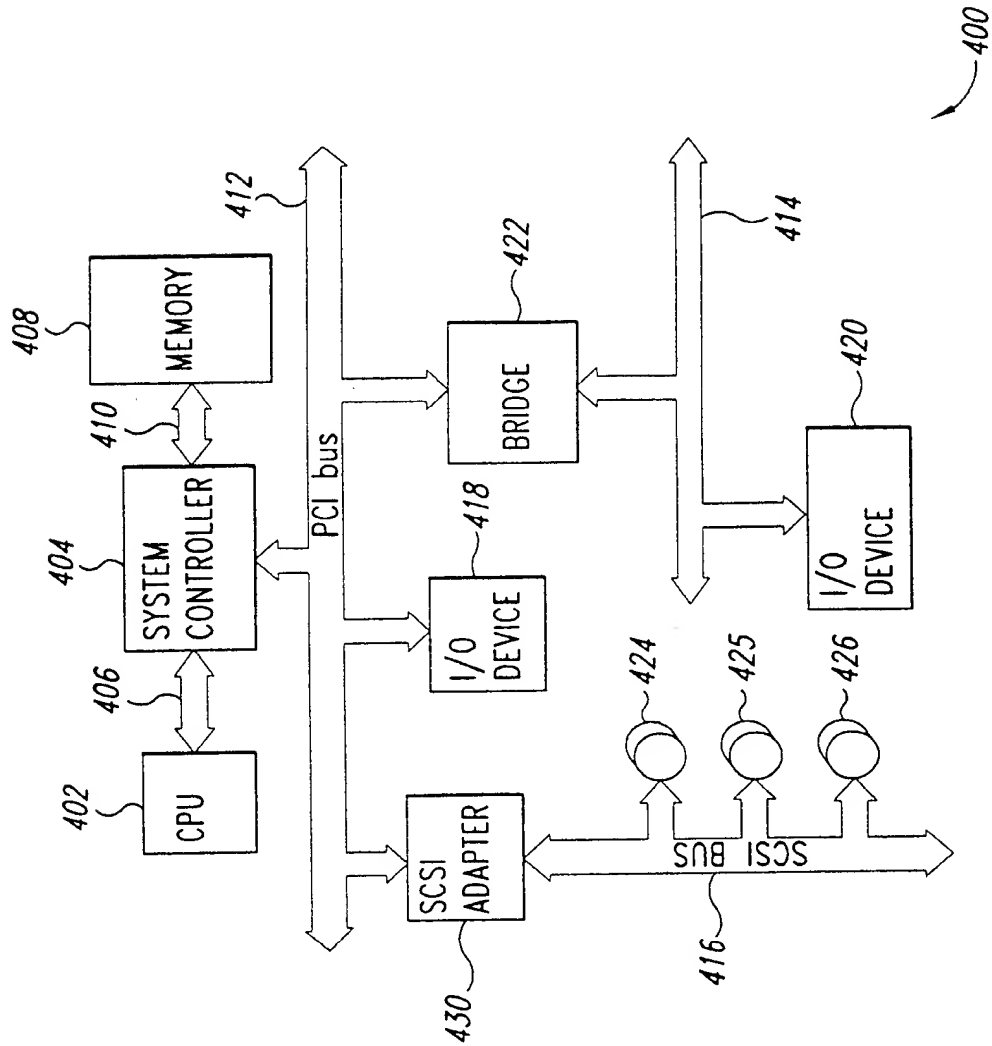


Fig. 4

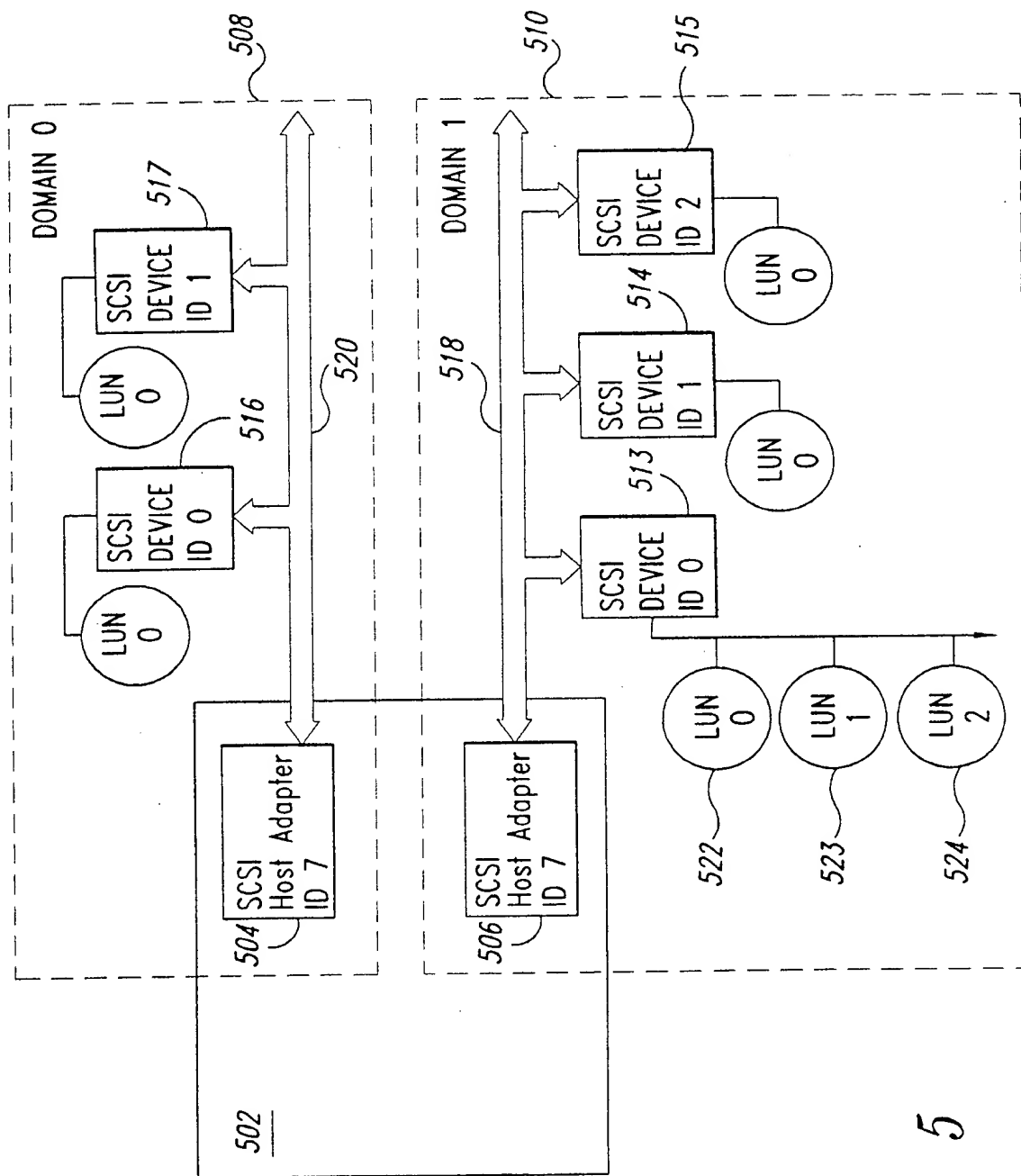


Fig. 5

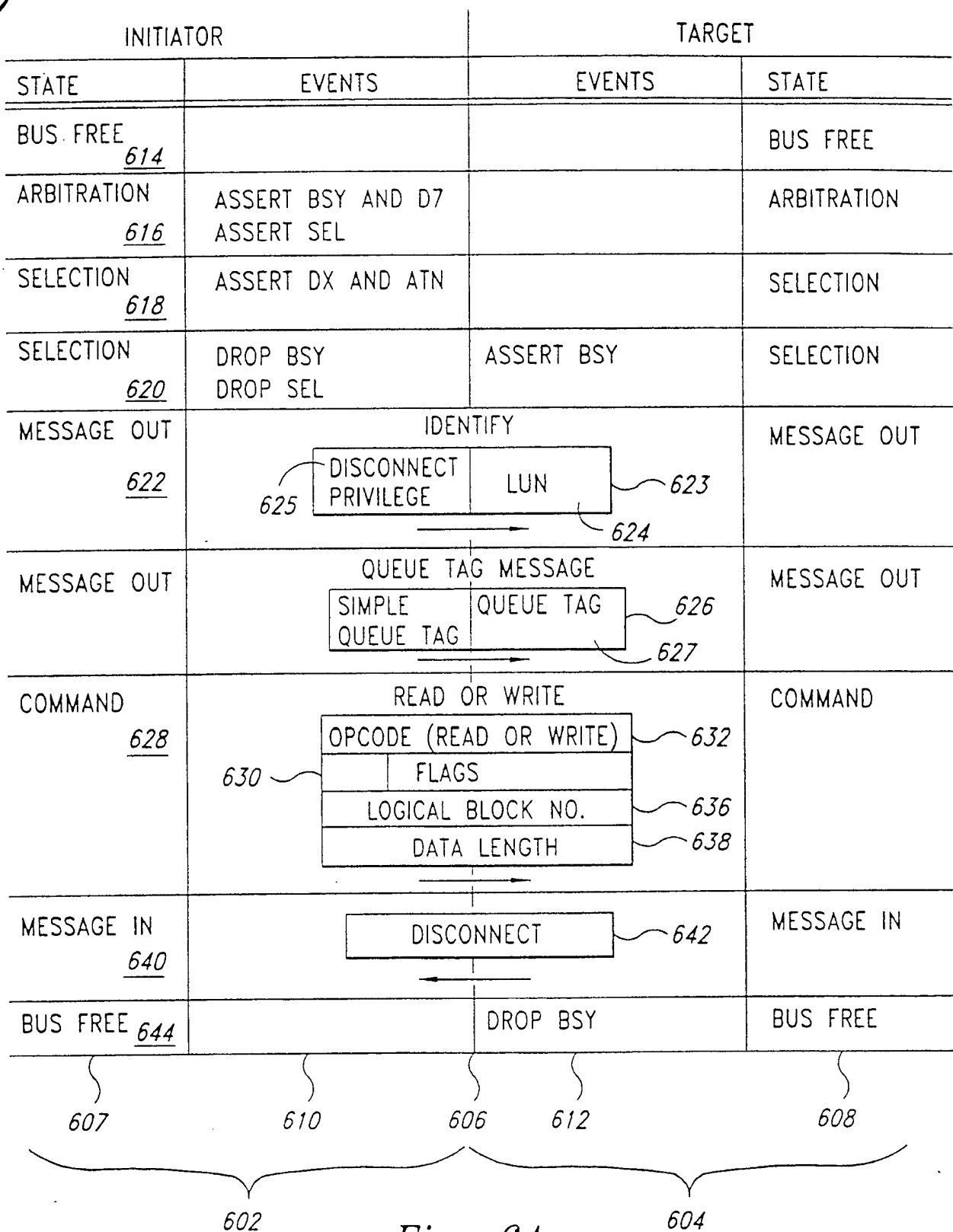


Fig. 6A

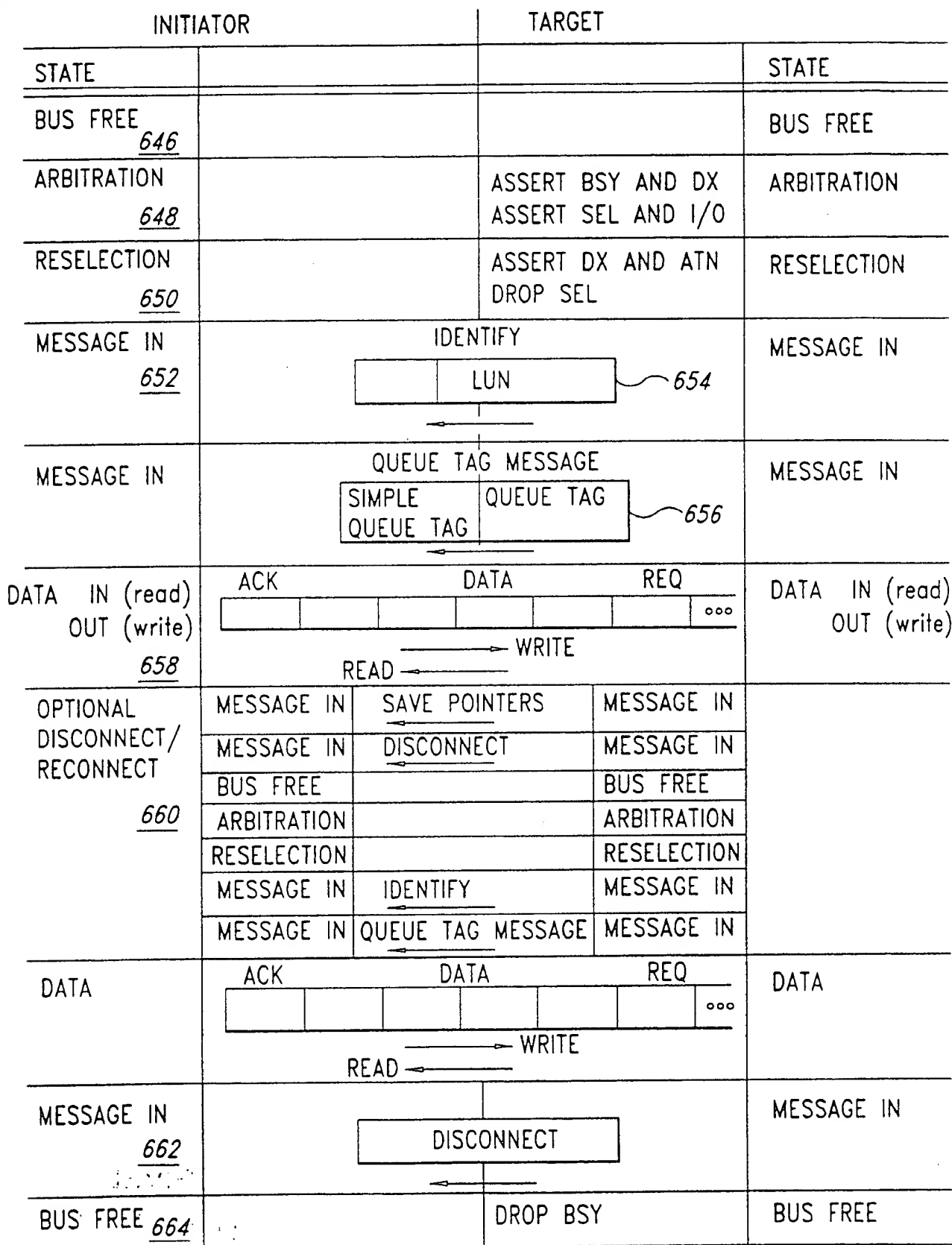


Fig. 6B



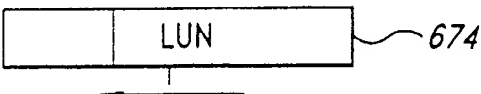

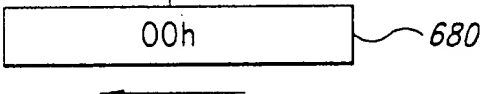
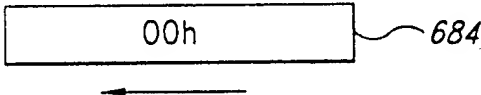
| INITIATOR | | TARGET | |
|---------------------------|----------------------------------------------------------------------------------------------------------|-----------------------------------------|-------------|
| state | | | state |
| BUS FREE <u>666</u> | | | BUS FREE |
| ARBITRATION <u>668</u> | | ASSERT BSY AND DX ASSERT SEL AND I/O | ARBITRATION |
| RESELECTION <u>670</u> | | ASSERT DX AND ATN DROP SEL | RESELECTION |
| MESSAGE IN <u>672</u> | IDENTIFY  | | MESSAGE IN |
| MESSAGE IN | QUEUE TAG MESSAGE  | | MESSAGE IN |
| STATUS <u>678</u> |  | | STATUS |
| MESSAGE IN <u>682</u> | COMMAND COMPLETE  | | MESSAGE IN |
| BUS FREE <u>686</u> | | DROP BSY | BUS FREE |

Fig. 6C